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TOPFET dual high side switch BUK218-50DC

DESCRIPTION

Monolithic dual channel high side protected power switch in TOPFET2 technology assembled in a 7 pin plastic surface mount package.

APPLICATIONS

General purpose switch for driving lamps, motors, solenoids, heaters.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MIN.	UNIT
I_L	Nominal load current (ISO)	8	A
SYMBOL	**PARAMETER**	**MAX.**	**UNIT**
V_{BG}	Continuous off-state supply voltage	50	V
I_L	Continuous load current	16	A
T_j	Continuous junction temperature	150	˚C
R_{ON}	On-state resistance, T_j = 25˚C	40	mΩ

FEATURES

- Vertical power TrenchMOS
- Low on-state resistance
- CMOS logic compatible
- Very low quiescent current
- Overtemperature protection
- Load current limiting
- Overload and short circuit protection
- Self resetting overcurrent protection
- Overvoltage and undervoltage shutdown with hysteresis
- Off-state open circuit load detection
- Diagnostic status indication
- Voltage clamping for turn off of inductive loads
- ESD protection on all pins
- Reverse battery, overvoltage and transient protection

FUNCTIONAL BLOCK DIAGRAM

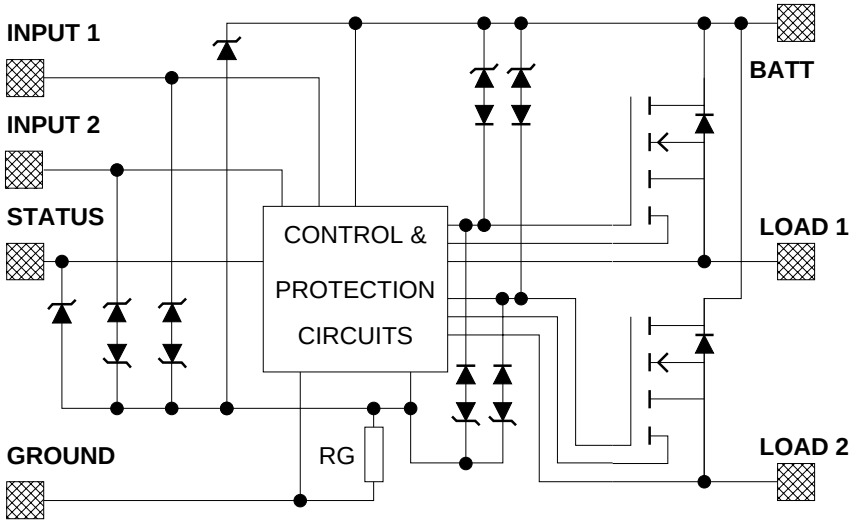


Fig.1. Elements of the TOPFET dual HSS with internal ground resistor.

PINNING - SOT427

PIN	DESCRIPTION
1	load 1
2	ground
3	input 1
4	connected to mb
5	status
6	input 2
7	load 2
mb	battery

PIN CONFIGURATION

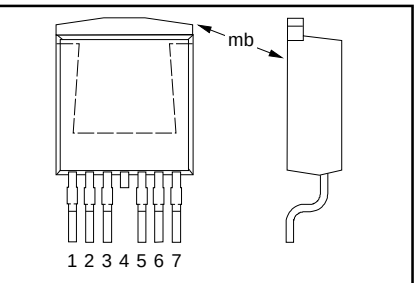


Fig. 2.

SYMBOL

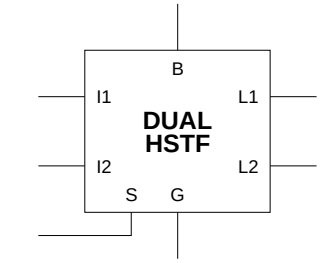


Fig. 3.

CONVENTION

Positive currents flow into pins, except for load and ground pins.

LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{BG}	Continuous supply voltage		0	50	V
I_L	Continuous load current per channel	$T_{mb} \leq 135^{\circ}C$	-	8	A
P_D	Total power dissipation	$T_{mb} \leq 25^{\circ}C$	-	83.3	W
T_{stg}	Storage temperature		-55	175	$^{\circ}C$
T_j	Continuous junction temperature[1]		-40	150	$^{\circ}C$
	Reverse battery voltages[2]				
V_{GB}	Continuous reverse voltage		-	16	V
V_{GB}	Peak reverse voltage		-	32	V
	Application information				
R_I, R_S	External resistors[3]	to limit input, status currents	3.2	-	kΩ
	Input and status currents				
I_I	Continuous input current		-5	5	mA
I_S	Continuous status current		-5	5	mA
I_I	Repetitive peak input current	$\delta \leq 0.1$, t_p = 300 μs	-50	50	mA
I_S	Repetitive peak status current	$\delta \leq 0.1$, t_p = 300 μs	-50	50	mA
	Inductive load clamping	V_{BG} = 13 V, I_L = 8 A			
E_{BL}	Non-repetitive clamping energy (one channel)	T_j = 150˚C prior to turn-off	-	150	mJ

ESD LIMITING VALUE

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_C	Electrostatic discharge capacitor voltage	Human body model; C = 250 pF; R = 1.5 kΩ	-	2	kV

1 For normal continuous operation. A higher T_j is allowed as an overload condition but at the threshold $T_{j(TO)}$ the over temperature trip operates to protect the switch.

2 Reverse battery voltage is allowed only with external resistors to ensure that the input and status currents do not exceed the limiting values. The internal ground resistor limits the reverse battery ground current. The connected loads must limit the reverse load currents. Power is dissipated and the T_j rating must be observed.

3 To limit currents during reverse battery and transient overvoltages (positive or negative).

THERMAL CHARACTERISTIC

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Thermal resistance[1]					
$R_{th\ j\text{-}mb}$	Junction to mounting base	per channel	-	2.4	3	K/W
		both channels	-	1.2	1.5	K/W

STATIC CHARACTERISTICS

Limits are at -40˚C ≤ T_{mb} ≤ 150˚C and typicals at T_{mb} = 25 ˚C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS				MIN.	TYP.	MAX.	UNIT
	Clamping voltages								
V_{BG}	Battery to ground	I_G = 1 mA				45	55	65	V
V_{BL}	Battery to load per channel	$I_L = I_G$ = 1 mA				50	55	65	V
V_{GL}	Ground to load[2]	I_L = 10 mA				18	23	28	V
		I_L = 10 A; t_p = 300 µs				20	25	30	V
	Supply voltage	battery to ground							
V_{BG}	Operating range[3]	-				5.5	-	35	V
	Currents	9 V ≤ V_{BG} ≤ 35 V							
I_B	Total quiescent current[4]	V_{LG} = 0 V				-	-	20	µA
					T_{mb} = 25˚C	-	0.1	1	µA
I_L	Off-state load current per	$V_{BL} = V_{BG}$				-	-	10	µA
	channel				T_{mb} = 25˚C	-	0.1	1	µA
I_G	Operating current	one channel on				-	1.8	3	mA
		both channels on				-	3.6	6	mA
I_L	Nominal load current[5]	V_{BL} = 0.5 V; T_{mb} = 85˚C				8	-	-	A
R_G	Effective internal ground resistance[6]	I_G = -200 mA; t_p = 300 µs				40	75	100	Ω
	Resistances per channel	V_{BG}	I_L	t_p[7]	T_j				
R_{ON}	On-state resistance	9 to 35 V	10 A	300 µs	25˚C	-	30	40	mΩ
					150˚C	-	60	80	mΩ
R_{ON}	On-state resistance	5.5 V	5 A	300 µs	25˚C	-	50	60	mΩ
					150˚C	-	100	120	mΩ

1 Of the output Power MOS transistors.

2 For a high side switch, the load pin voltage goes negative with respect to ground during the turn-off of an inductive load. This negative voltage is clamped by the device.

3 On-state resistance is increased if the supply voltage is less than 7 V.

4 This is the continuous current drawn from the battery when both inputs are low and includes leakage currents to the loads.

5 Per channel but with both channels conducting. Defined as in ISO 10483-1.

6 Equivalent of the parallel connected resistors for both channels.

7 The supply and input voltage for the R_{ON} tests are continuous. The specified pulse duration t_p refers only to the applied load current.

INPUT CHARACTERISTICS

5.5 V ≤ V_{BG} ≤ 35 V. Limits are at -40˚C ≤ T_{mb} ≤ 150˚C and typicals at T_{mb} = 25˚C unless otherwise stated.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_I	Input current	V_{IG} = 5 V	20	60	160	µA
V_{IG}	Input clamping voltage	I_I = 200 µA	5.5	7	8.5	V
$V_{IG(ON)}$	Input turn-on threshold voltage		-	2.1	3	V
$V_{IG(OFF)}$	Input turn-off threshold voltage		1.2	1.8	-	V
ΔV_{IG}	Input turn-on hysteresis		0.15	0.3	0.5	V
$I_{I(ON)}$	Input turn-on current	V_{IG} = 3 V	-	-	100	µA
$I_{I(OFF)}$	Input turn-off current	V_{IG} = 1.2 V	12	-	-	µA

OPEN CIRCUIT DETECTION CHARACTERISTICS

An open circuit load on either channel can be detected in the off-state. Refer to TRUTH TABLE.
This feature requires external load pull-up to a positive supply voltage via a suitable resistor.
Limits are at -40˚C ≤ T_{mb} ≤ 150˚C and typical is at T_{mb} = 25˚C.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Open circuit detection					
$V_{LG(OC)}$	Load ground threshold voltage	V_{BG} ≥ 9 V	1.5	2.5	3.5	V
$I_{B(OC)}$	Supply quiescent current per OC channel	V_{BG} = V_{LG} = 16 V open circuit detected, other channel off	-	0.8	1.5	mA
$-I_{L(OC)}$	Load ground current per channel	V_{LG} = 16 V	-	200	300	µA
		V_{LG} = 3.5 V	-	22	40	µA
$t_{d(OC)}$	Status delay time	input low to status low	-	65	100	µs
	Application information					
R_{ext}	External load pull-up resistance per channel	V_{ext} = 5 V	-	10	-	kΩ

UNDERVOLTAGE & OVERVOLTAGE CHARACTERISTICS

Limits are at -40˚C ≤ T_{mb} ≤ 150˚C and typicals at T_{mb} = 25˚C. Refer to TRUTH TABLE.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Undervoltage					
$V_{BG(UV)}$	Low supply threshold voltage[1]		2	4.2	5.3	V
$\Delta V_{BG(UV)}$	Hysteresis		0.1	0.5	1	V
	Overvoltage					
$V_{BG(OV)}$	High supply threshold voltage[2]		35	40	45	V
$\Delta V_{BG(OV)}$	Hysteresis		0.4	1	2	V
$I_{BG(OV)}$	Operating current per channel	$V_{BG} > V_{BG(OV)}$	-	1	2	mA

OVERLOAD PROTECTION CHARACTERISTICS

Independent protection per channel. Refer to TRUTH TABLE.
5.5 V ≤ V_{BG} ≤ 35 V, limits are at -40˚C ≤ T_{mb} ≤ 150˚C and typicals at T_{mb} = 25˚C unless otherwise stated.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Overload protection	$V_{BL} = V_{BG}$; t_p = 300 µs				
$I_{L(lim)}$	Load current limiting	$V_{BG} \geq 8$ V	18	30	42	A
		V_{BG} = 5.5 V	15	27	42	A
	Short circuit load protection	T_{mb} ≤ 125˚C prior to overload[3]				
$P_{D(TO)}$	Overload power threshold	for protection[4]	100	150	200	W
T_{DSC}	Characteristic time	which determines trip time[5]	-	200	500	µs
	Overtemperature protection					
$T_{j(TO)}$	Threshold junction temperature		150	170	190	˚C
$\Delta T_{j(TO)}$	Hysteresis[6]		3	10	20	˚C

1 Undervoltage sensors causes each channel to switch off and reset.

2 Overvoltage sensors causes each output channel to switch off to protect its load.

3 Above this temperature measurement of these parameters is prevented because OT protection may occur prior to SC protection.

4 Normal operation will be resumed when $P_D < P_{D(TO)}$ and $T_j < T_{j(TO)}$.

5 Trip time $t_{d\,sc}$ varies with overload dissipation P_D according to the *exponential model* formula $t_{d\,sc} \approx T_{DSC} / LN[P_D / P_{D(TO)}]$.

6 After cooling below the reset temperature the channel will resume normal operation.

STATUS CHARACTERISTICS

The status output is an open drain transistor, and requires an external pull-up circuit to indicate a logic high. Limits are at -40˚C ≤ T_{mb} ≤ 150˚C and typicals at T_{mb} = 25˚C unless otherwise stated. Refer to TRUTH TABLE.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{SG}	Status clamping voltage	I_S = 100 µA	5.5	7	8.5	V
$V_{SG(LO)}$	Status low voltage	I_S = 100 µA	-	0.7	0.9	V
		I_S = 250 µA	-	-	1.1	V
I_S	Status leakage current	V_{SG} = 5 V	-	-	10	µA
		T_{mb} = 25˚C	-	0.1	1	µA
$I_{S(SAT)}$	Status saturation current[1]	V_{SG} = 5 V	5	10	15	mA
	Application information					
R_S	External pull-up resistor		-	47	-	kΩ

TRUTH TABLE

INPUT		ABNORMAL CONDITIONS DETECTED								LOAD OUTPUT		STATUS	DESCRIPTION
		SUPPLY		LOAD 1			LOAD 2						
1	2	UV	OV	OC	SC	OT	OC	SC	OT	1	2		
L	L	0	X	0	X	X	0	X	X	OFF	OFF	H	both off & normal
L	L	0	X	1	X	X	X	X	X	OFF	OFF	L	both off, one/both OC or short to V+
L	H	0	X	1	X	X	0	0	0	OFF	ON	L	one off & OC, other on & normal
H	L	0	0	0	0	0	0	0	0	ON	OFF	H	one on & normal, other off & normal
H	H	0	0	0	0	0	0	0	0	ON	ON	H	both on & normal
H	X	1	0	X	X	X	0	X	X	OFF	OFF	H	supply undervoltage lockout
H	X	0	1	X	0	0	X	0	0	OFF	OFF	H	supply overvoltage shutdown
H	X	0	0	0	1	X	X	X	X	OFF	X	L	one SC shutdown
H	L	0	0	0	1	X	0	0	X	OFF	OFF	L	one SC shutdown, other off & normal
H	H	0	0	0	1	X	0	0	0	OFF	ON	L	one SC shutdown, other on & normal
H	X	0	0	0	0	1	X	X	X	OFF	X	L	one OT shutdown
H	L	0	0	0	0	1	0	0	X	OFF	OFF	L	one OT shutdown, other off & normal
H	H	0	0	0	0	1	0	0	0	OFF	ON	L	one OT shutdown, other on & normal

KEY TO ABBREVIATIONS

L	logic low	UV	undervoltage
H	logic high	OV	overvoltage
X	don't care	OC	open circuit
0	condition not present	SC	short circuit
1	condition present	OT	overtemperature

1 For example with the pull-up resistor short circuited while the status transistor is conducting. This condition should be avoided in order to prevent possible interference with normal operation of the device.

SWITCHING CHARACTERISTICS

T_{mb} = 25 ˚C, V_{BG} = 13 V, for resistive load R_L = 13 Ω per channel.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	During turn-on	from input going high				
$t_{d\ on}$	Delay time	to 10% V_L	-	30	-	µs
dV/dt_{on}	Rate of rise of load voltage	30% to 70% V_L	0.5	1	2	V/µs
$t_{\ on}$	Total switching time	to 90% V_L	-	100	400	µs
	During turn-off	from input going low				
$t_{d\ off}$	Delay time	to 90% V_L	-	20	-	µs
dV/dt_{off}	Rate of fall of load voltage	70% to 30% V_L	0.5	1	2	V/µs
$t_{\ off}$	Total switching time	to 10% V_L	-	40	200	µs

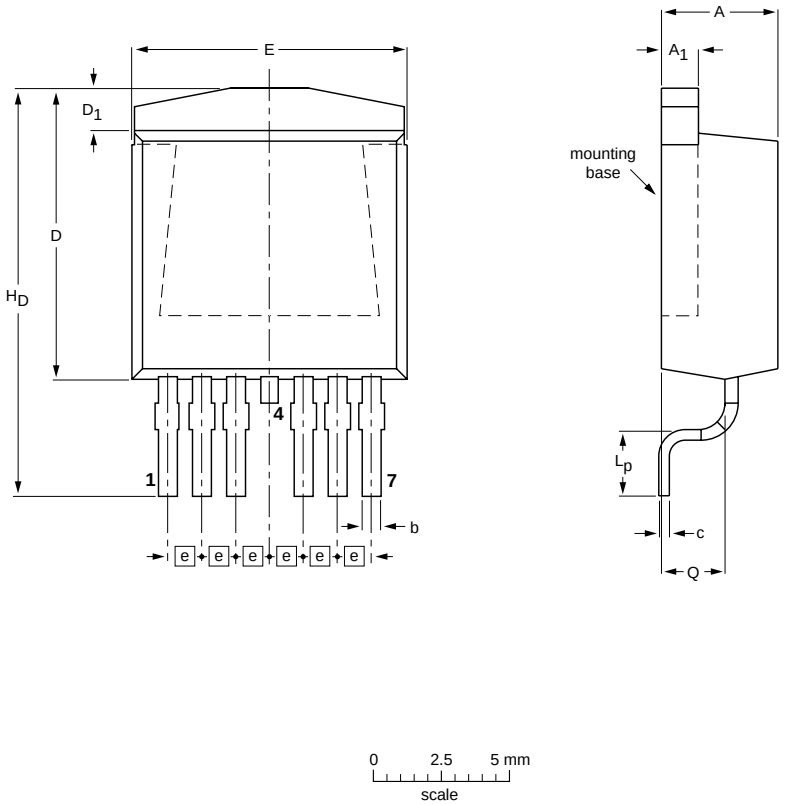
CAPACITANCES

T_{mb} = 25 ˚C; f = 1 MHz; V_{IG} = 0 V

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
C_{sg}	Status capacitance	V_{SG} = 5 V	-	11	15	pF
	per channel					
C_{ig}	Input capacitance	V_{BG} = 13 V	-	15	20	pF
C_{bl}	Output capacitance	V_{BL} = 13 V	-	265	375	pF

MECHANICAL DATA

Plastic single-ended surface mounted package (Philips version of D^2-PAK); 7 leads (one lead cropped) **SOT427**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1	b	c	D max.	D_1	E	e	L_p	H_D	Q
mm	4.50 4.10	1.40 1.27	0.85 0.60	0.64 0.46	11	1.60 1.20	10.30 9.70	1.27	2.90 2.10	15.80 14.80	2.60 2.20

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT427						~~99-06-25~~ 01-04-18

Fig.4. SOT427 surface mounting package[1], centre pin connected to mounting base.

1 Epoxy meets UL94 V0 at 1/8". Net mass: 1.5 g.
For soldering guidelines and SMD footprint design, please refer to Data Handbook SC18.

DEFINITIONS

DATA SHEET STATUS		
DATA SHEET STATUS[1]	**PRODUCT STATUS**[2]	**DEFINITIONS**
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A
Limiting values		
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.		
Application information		
Where application information is given, it is advisory and does not form part of the specification.		
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